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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	09/928,797
Filing Date	08/13/2001
First Named Inventor	Xiaoning Nie
Art Unit	2185
Examiner Name	
Attorney Docket Number	1406/13

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	<i>David Martinez</i>	Date Considered	8/13/03
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/08B (10-01)
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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/928,797
		Filing Date	08/13/2001
		First Named Inventor	Xiaoning Nie
		Group Art Unit	2185
		Examiner Name	
Sheet 1 of 1	Attorney Docket Number	1406/13	

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
Dm	B1	DELGADO-FRIAS & DIAZ, "A VLSI Self-Compacting Buffer for DAMQ Communication Switches," IEEE Eighth Great Lakes Symposium on VLSI, p. 128-133, (February 1998).	X
Dm	B2	ANONYMOUS, "TMS320C3x User's Guide," Texas Instruments Inc., p. i-xix, 2-1 - 2-27, 13-121 - 13-132, (July 1997).	X
Dm	B3	PARK ET AL., "Design and Evaluation of a DAMQ Multiprocessor Network With Self-Compacting Buffers," p. 713-722, (1994).	X
Dm	B4	IBM TECHNICAL DISCLOSURE BULLETIN, "Efficient GPR Using Integrated Dual 2-Port Array," p. 7249-7251, (May 1985).	X
Dm	B5	IBM TECHNICAL DISCLOSURE BULLETIN, "Local Storage Organization with Simultaneous Read and Write," p. 7049-7050, (May 1985).	X

Examiner Signature	<i>David Martinez</i>	Date Considered	8/13/03
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